SPECIFICATION AMENDMENTS

Amend the paragraph that begins in line 17 on page 6 as follows:

According to a sixth aspect of the present invention, the semiconductor switch circuit according to any one of the first through fifth aspects is used as intersection switches, and forms a matrix circuit.

Amend the paragraph that begins in line 1 on page 17 as follows:

When the second switch SW2 and the third switch SW3 are controlled to the OFF state, and the switch SW5 is controlled to the ON state by the switch control means SCON (Fig. 5), a non-conducting state exists between the input terminal IN and the output terminal OUT, and the potential of the output terminal OUT is applied to the junction K by the operation of the second voltage application means M2. Consequently, the voltages (+V and 0) or (−V and 0) applied between the input terminal IN and the output terminal OUT are applied to both ends of the second switch SW2 at this time, and a leak current (I1 or I2) flows through the second switch SW2 due to this voltage; however, this leak current flows out through the direct current amplifier A2 constituting the second voltage application means M2, or flows in through the direct current amplifier ~~A1~~ A2. This leak current therefore flows through the input terminal IN side, but does not flow through the output terminal OUT side (note that the current paths are depicted in Fig. 5 based on Figs. 2A and 2B).